

Office Action Summary	Application No. 10/528,474	Applicant(s) TEE ET AL.	
	Examiner NIKETA I. PATEL	Art Unit 2181	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 24 March 2009.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-15 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-15 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 8/7/2008 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☒ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|--|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input checked="" type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. <u>20090604</u> . |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____. | 6) <input type="checkbox"/> Other: _____. |

DETAILED ACTION

Response to Arguments

1. Applicant's arguments filed 3/24/2009 have been fully considered but they are not persuasive. Applicant argues that there is no evidence which indicates that the TransDimension reference was accessible to the public before Applicant's priority date of September 24, 2002. Furthermore, the TransDimension reference is marked "preliminary" and "proprietary" which indicates that the reference was not accessible to the public as of March 25, 2002.

Examiner respectfully disagrees with this argument. During the telephone conversation the examiner stated that the reference was found on the World Wide Web (WWW) database via GOOGLE search engine however, the most recent search of the WWW database did not yield the precise website address of the reference. Therefore it appears as though the reference may not be valid and further review is required. Despite the result, the reference cannot be ignored. Since, the examiner was able to find it when the Office Action dated 12/24/2008 was issued and the fact that the reference has a publication date of March 25, 2002. Furthermore, the fact that the reference is marked "preliminary" and "proprietary" does not render the reference not accessible to the public. Examiner has found another TransDimension product brief document (attached herewith) which is still available to this date with "proprietary" marking. The "preliminary" marking only implies that the document is in preliminary stage and that there might be other versions with/without modifications. With this in

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mind, the examiner has determined that the rejection set forth in the previous Office Action is maintained.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

3. Claims 1-15 are rejected under 35 U.S.C. 102(a) as being anticipated by TransDimension OTG243 Product Brief (hereinafter "TransDimension".)

4. Referring to claims 1, 3, 5, 6, taking claim 1 as exemplary, TransDimension teaches an interface integrated circuit device for interfacing a USB connection to a further circuit, the interface integrated circuit comprising: first external terminals for connecting to a USB bus [figure 1, element "USB transceiver" and page 1, column 1]; a transceiver capable of transceiving for both a USB host and a USB device [figure 1, element "OTG transceiver"], the transceiver having a USB interface [figure 1, element "Function SIE", multiplexer, "OTG transceiver; and page 1, column 1], a host interface and a device interface, the USB interface being coupled to the first external terminals [figure 1, element "Host SIE & Root Hub", multiplexer, "OTG transceiver; and page 1, column 1]; second external terminals coupled to the device interface for connection to an external USB device controller [figure 1, element "USB transceiver"]; a host controller coupled to the host interface [figure 1, element "USB Host Control Logic", "Host SIE & Root Hub"], the host controller having a parallel data/address bus [figure 1,

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element “Microprocessor interface”, “USB Host Control Logic” and page 3, description of “microprocessor interface”]; third external terminals coupled to the parallel data/address bus [figure 1, element “Microprocessor interface” and pages 1-4.]

5. Referring to claim 2, TransDimension teaches an integrated circuit device according to claim 1, wherein the device interface comprises both an analog USB device interface and a transceived digital USB device interface for connection to an external USB device controller without and with an external transceiver, respectively [figure 1 and pages 1-4.]

6. Referring to claim 4, TransDimension teaches an electronic apparatus according to claim 3, wherein the apparatus is arranged to use USB communication from said host controller via the USB connection in a first speed mode when operating as USB host and to use USB communication via the USB connection in a second speed mode, different from said first speed mode, as determined by the device controller when operating as USB device [figure 1 and pages 1-4.]

7. Referring to claim 7, TransDimension teaches a method according to claim 6, wherein passing USB signals from the transceiver to the device controller in the functional circuits outside the integrated circuit includes passing analog USB signals from the transceiver to the device controller when the device controller includes a transceiver and passing transceived digital USB signals from the transceiver to the device controller when the device controller does not include a transceiver [figure 1 and pages 1-4.]

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8. Referring to claim 8, TransDimension teaches a method according to claim 6, wherein the interface integrated circuit does not include a USB device controller [figure 1 and pages 1-4.]

9. Referring to claim 9, TransDimension teaches an interface integrated circuit device according to claim 1, wherein the interface integrated circuit does not include a USB device controller [figure 1 and pages 1-4.]

10. Referring to claim 10, TransDimension teaches an interface integrated circuit device according to claim 1, wherein the interface integrated circuit is arranged to use USB communication from said host controller via the USB connection in a first speed mode when operating as a USB host and to use USB communication via the USB connection in a second speed mode, different from said first speed mode, as determined by the device controller when operating as a USB device [figure 1 and pages 1-4.]

11. Referring to claim 11, TransDimension teaches an electronic apparatus according to claim 3, wherein the device interface includes both an analog USB device interface and a transceived digital USB device interface for connection to the USB device controller circuit without and with an external transceiver, respectively [figure 1 and pages 1-4.]

12. Referring to claim 12, TransDimension teaches an electronic apparatus according to claim 3, wherein the interface integrated circuit does not include a USB device controller [figure 1 and pages 1-4.]

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13. Referring to claim 13, TransDimension teaches an electronic system according to claim 5, wherein the device interface includes both an analog USB device interface and a transceived digital USB device interface for connection to the USB device controller circuit without and with an external transceiver, respectively [figure 1 and pages 1-4.]

14. Referring to claim 14, TransDimension teaches an electronic system according to claim 5, wherein the interface integrated circuit does not include a USB device controller [figure 1 and pages 1-4.]

15. Referring to claim 15, TransDimension teaches an electronic system according to claim 5, wherein the system is arranged to use USB communication from said host controller via the USB connection in a first speed mode when operating as a USB host and to use USB communication via the USB connection in a second speed mode, different from said first speed mode, as determined by the device controller when operating as a USB device [figure 1 and pages 1-4; full speed and low speed.]

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to NIKETA I. PATEL whose telephone number is (571)272-4156. The examiner can normally be reached on M-F 8:00 A.M. to 6:00 P.M, with every other Friday off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Alford Kindred can be reached on (571) 272 4037. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Niketa I. Patel/
Primary Examiner, Art Unit 2181